

ULSI Workshop 2023 Program (27th May, 2024)

Venue: Masaryk University Department of Mathematics and Statistics, Building 08		Presentation No.	Regular Session (15 minutes presentation and 3 minutes Q&A) *Please use your own computer for the presentation.	Names	Affrecaation
Time					
Regular Session	14:30 ~ 14:48	1	A 2.4GHz compact Transformer-Based 90° Hybrid PA in 65nm CMOS	Ryuji Oka, Akira Hyogo, Tatsuji Matsuura, Ryoitchi Miyauchi	<i>Tokyo University of Science</i>
	14:48 ~ 15:06	2	Improving linearity of a current bleeding mixer using DS method with a 65nm CMOS process	Kazumasa Tsutsumi, Ryoichi Miyauchi, Akira Hyogo	<i>Tokyo University of Science</i>
	15:06 ~ 15:24	3	Multi-valued transmission evaluation by two-dimensional mapping using a digital oscilloscope	Atsunori Okada*, Yosuke Iijima*, Yasushi Yuminaka**	<i>*National Institute of Technology(KOSEN), Oyama college, **Gunma University</i>
	15:24 ~ 15:42	4	Design principle of 2.4-GHz-band source-degenerated cascode low-noise amplifiers using a high linearization technique by elimination of third-order harmonic distortion	Shunsuke Kaise, Ryoichi Miyauchi, Akira Hyogo	<i>Tokyo University of Science</i>
	15:42 ~ 16:00	5	Improvement of power conversion efficiency at wide input power in a cross-coupled differential CMOS rectifier circuit	Tatsumi Hashimoto, Tatsuji Matsuura, Ryoichi Miyauchi, Akira Hyogo	<i>Tokyo University of Science</i>
	16:00 ~ 16:18	6	Toward formal verification of arithmetic circuits with states based on ZDD representation	Kazuho SAKODA, Yasuyoshi UEMURA, and Naofumi HOMMA	<i>Tohoku University</i>
	16:18 ~ 16:36	7	An Evaluation Circuit for Hot Carrier Injection (HCI) Using Stage Switching Function	Risa.Toda, Ryo Kishida, Kazutoshi Kobayashi, Tatsuji Matsuura, Ryoichi Miyauchi, Akira Hyogo	<i>Tokyo University of Science</i>
	16:36 ~ 16:54	8	Consideration of the Influence of Control Signals in the SSHI on the Efficiency of the Interface Circuit	Shu Osawa, Ryoichi Miyauchi, Akira Hyogo	<i>Tokyo University of Science</i>